

Surface Mount Compound Function Triggers

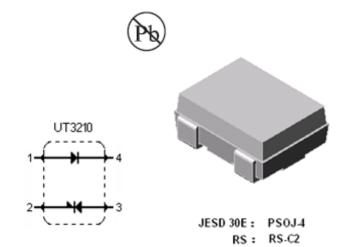
Features

- Included a trigger chip and a rectifier chip
- Glass passivated chip junctions
- Low leakage current

Mechanical Data

- Case: JESD 30E PSOJ-4 molded plastic body over glass passivated chip
- Terminals: Solder plated, solderable per J-STD-002B and JESD22-B102D

• Weight: 0.046g



Typical Applications

 For use in energy conservation electronic Ballasts fluorescent lamps

Maximum Ratings (T_A = 25 °C unless otherwise noted)

Parameter	Symbol	Value	Unit
Repetitive peak on-state current(tp=20µs F=100Hz) for Trigger Diode	I _{TRM}	2.0	Α
Power Dissipation for Trigger Diode (T _A =50℃)	P _D	150	mW
Maximum average forward rectified current for Rectifier	I _{F(AV)}	0.4	Α
Maximum repetitive peak reverse voltage for Rectifier	V_{RRM}	1000	V
Maximum non-repetitive peak reverse voltage for Rectifier	V_{RSM}	700	V
Peak forward surge current (8.3 ms half sine-wave) for Rectifier	I _{FSM}	10	Α
Thermal resistance from junction to ambient (Note 1)	$R_{\theta JA}$	112	°C/ W
Operating junction and storage temperature range	T _J , T _{stg}	- 40 ~ 125	$^{\circ}\!\mathbb{C}$

Note1: Mounted on FR-4 P.C.B. with soldering pad.

Electrical Characteristics for Trigger diode(T_A = 25 °C unless otherwise noted)

Parameter	Symbol	Min	Type	Max	Unit
r arameter	Зуппоп	141111	туре	IVIAA	Ollit
Breakover voltage at C=22nF	V_{BO}	28	-	36	V
Dynamic breakover voltage at $\Delta I = [I_{BO} \text{ to } I_F = 10\text{mA}]$	△V	5	10	-	V
Breakover current at C=22nF	I _{BO}	-	-	10	μA
Leakage current at V _B =0.5V _{BO} max	I _B	-	-	1.0	μA
Reverse current at V _R =6V	I _R	-	-	5.0	μA
Peak current (see diagram 4)	Ι _Ρ	1.0	-	-	Α
Rise time (see diagram 5)	t _r	-	-	1.5	μS

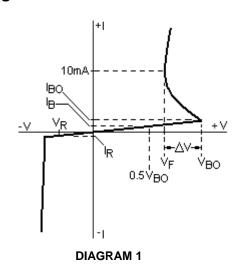
Electrical Characteristics for Rectifier (T_A = 25 °C unless otherwise noted)

Parameter	Symbol	Min	Туре	Max	Unit
Repetitive peak reverse voltage	V_{RRM}	1000	-	- /	V
Forward voltage at I _F =0.4A	V_{F}	-	0.93	1. 技7	中心
Reverse current at V _R =1000V	I _R	-	-	2.02014	.02,11
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Voltage-current characteristics for Trigger Diode



Marking

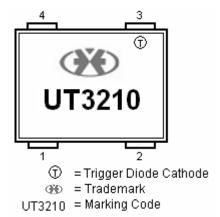
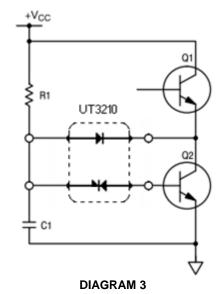


DIAGRAM 2

Recommended circuit connection



Test circuit for Trigger Diode

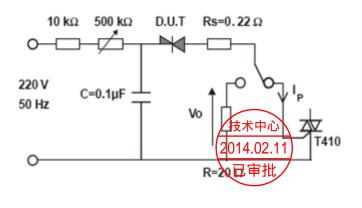
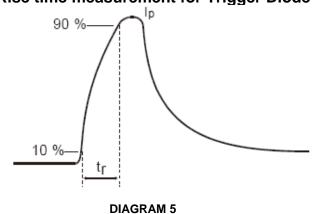


DIAGRAM 4

Rise time measurement for Trigger Diode



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Package Outline Dimensions in millimeters (inches)

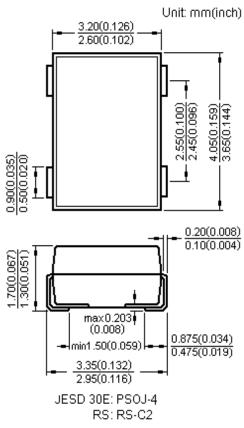


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Soldering Pad

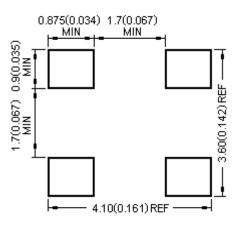


DIAGRAM 7

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